

**Abstract****A DECODER SUPPORTING MULTIPLE INPUTS**

- 5 A decoder has a plurality of tuners (110a-110d) for receiving data from different sources. Each tuner (110a-110d) has an associated demultiplexer (120a-120d) controlled to select a portion of a received signal corresponding to a selected channel or channels. The demultiplexers output to a remultiplexer (130; 210a-210c) configured to determine selected signal portions that have overlapping addresses, to reallocate addresses of the signal portions so there is no overlap, and to multiplex the signal portions for supply to a common interface slot (140; 220a-220c).
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[Fig. 2]

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